

TITLE 25G SFP28 Active Optical Cable	DOC No. RFD-20250717205-001	
	REVISION : 01	AUTHORIZED BY : Albert Lin
	DATE : 2025.07.17	CLASSIFICATION : Active Optical Cable

1. SCOPE

The 25G SFP28 AOC is intended for short reach service 25.78Gb/s 850nm Multi-mode high-speed communications equipment where low-cost, extraordinary performance and reliability are essential. It consumes low power, operates based on 3.3V DC power supply and is offered in the industrial temperature range. They are compliant with SFP28 MSA, SFF-8431 and SFF-8432.

The low jitter and low bit error rate optical assembly features a VCSEL laser transmitter and PIN/TIA receiver. It utilizes internal clock and data recovery (CDR) units on transmitter and the receiver chains for low jitter compliance. The differential AC coupled Tx and Rx data interfaces are CML compatible. The device is Class I laser safety compliant.

2. Features

- 25Gbps serial optical interface
- 850nm VCSEL transmitter and GaAs PIN PD receiver
- Case operating temperature range: 0°C to 70°C
- Power dissipation < 1.0W per cable end

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3. PRODUCT DESCRIPTION

3.1 PRODUCT NAME

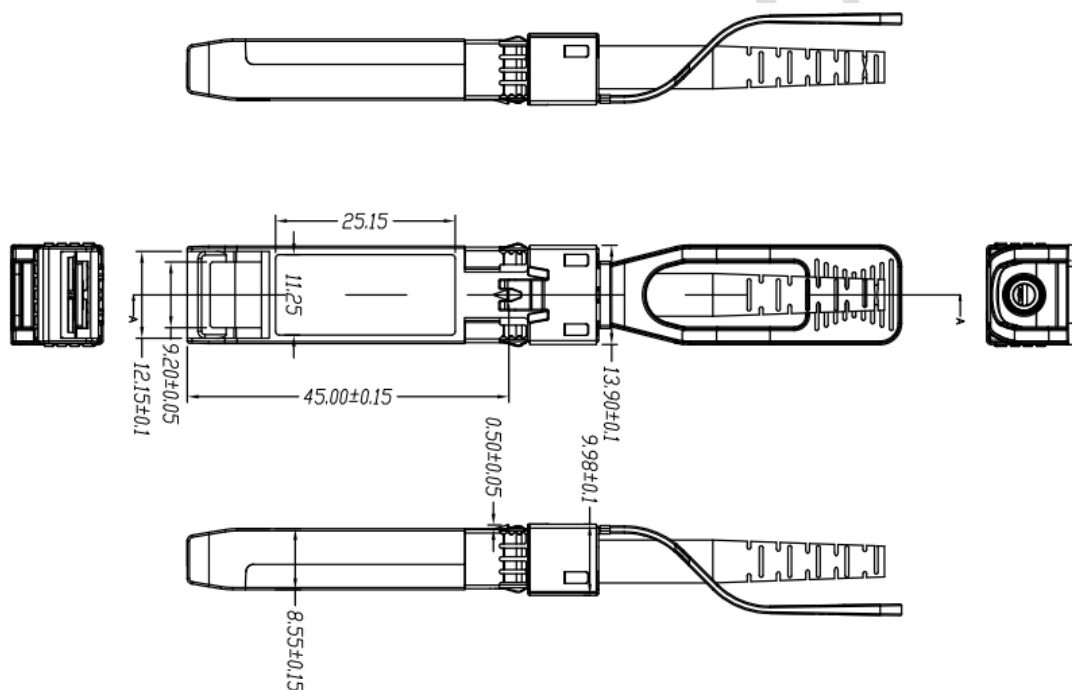
25G SFP28 Active Optical Cable

JPC P/N	Wavelength(nm)	Distance	Media	Cable
P57**RC00**M-1	850nm	~100m	MMF	MPO

Note-1 : ** Indicates the customer code.

Note-2 : ** Indicates the length.

3.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKING



SFP mechanical drawing (Unit: mm)

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4. APPLICABLE DOCUMENTS AND SPECIFICATIONS

- Inter Rack Connection
- High-speed Servers
- High-performance Computing Clusters
- SAN, Routers, Hubs, Load Balancer

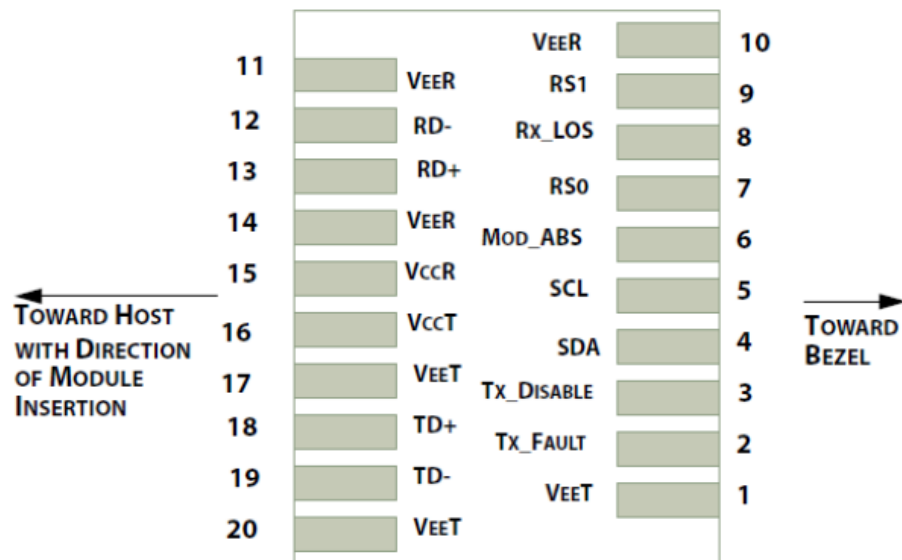
5. Production Specification

Absolute Maximum Ratings					
Parameter	Symbol	Min.	Max.	Unit	Notes
Storage Temperature	T _{Storage}	-40	+85	°C	
Relative Humidity	RH	0	+85	%	

Recommended Operating Conditions					
Parameter	Symbol	Min.	Typical	Max.	Unit
Case Temperature	T _c	0	-	70	°C
Power Supply Voltage	V _{cc}	3.15	3.3	3.45	V
Data Rate	-	-	25.78125	-	Gbps
Data Rate Accuracy	-	-100	-	100	ppm
Pre-FEC Bit Error Ratio	-	-	-	5e-5	
Post-FEC Bit Error Ratio	-	-	-	1e-12	
Power Consumption	-	-	-	1	W
Supply Current	I _{cc}	-	180	300	mA

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Applications Note :



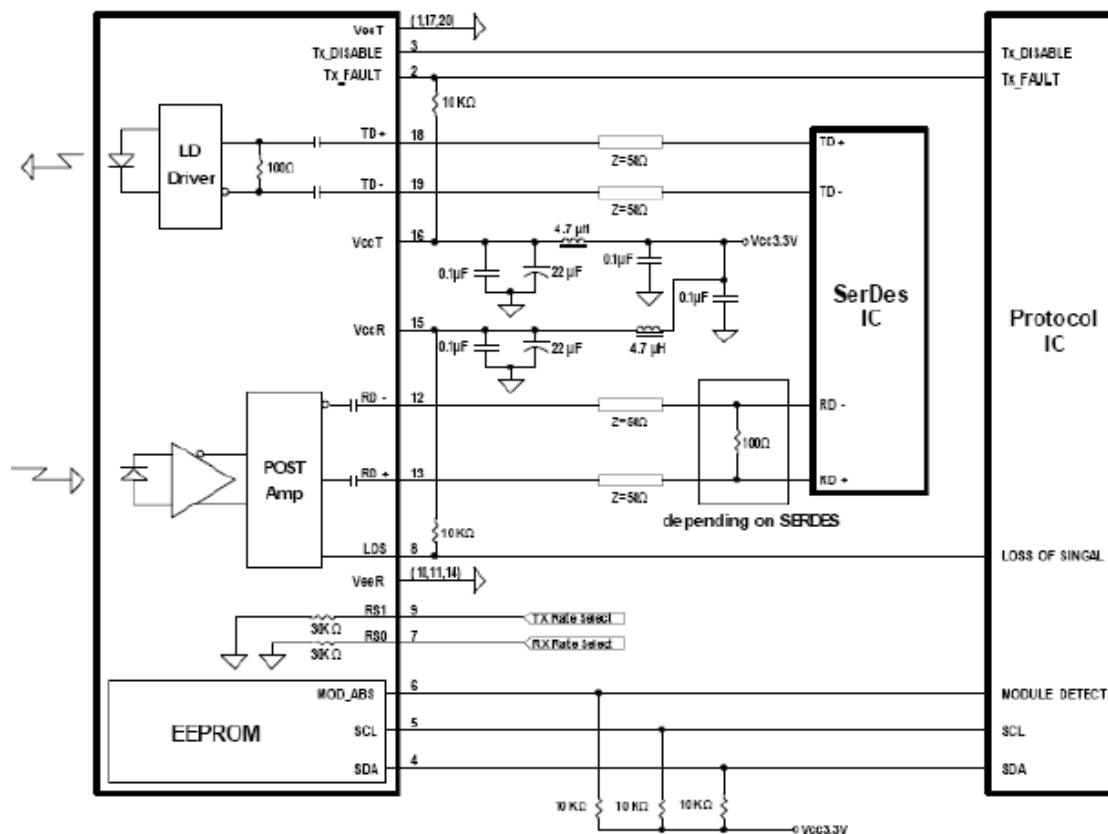
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Pin Assignment

Pin	Logic	Symbol	Name/Description	Note
1		VeeT	Module Transmitter Ground	
2	LVTTL-O	TX_Fault	Module Transmitter Fault	
3	LVTTL-I	TX_Disable	Transmitter Disable; Turns off transmitter laser output	
4	LVTTL-I/O	SDA	2-wire Serial Interface Data Line (Same as MOD-DEF2 as defined in the INF-8074i)	
5	LVTTL-I/O	SCL	2-wire Serial Interface Clock (Same as MOD-DEF1 as defined in the INF-8074i)	
6		MOD_ABS	Module Absent, connected to VeeT or VeeR in the module	
7	LVTTL-I	RS0	Not used	
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication (In FC designated as RX_LOS, in SONET designated as LOS, and in Ethernet designated at Signal Detect)	
9	LVTTL-I	RS1	Not used	
10		VeeR	Module Receiver Ground	
11		VeeR	Module Receiver Ground	
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Non-Inverted Data Output	
14		VeeR	Module Receiver Ground	
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Transmitter 3.3 V Supply	
17		VeeT	Module Transmitter Ground	
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	

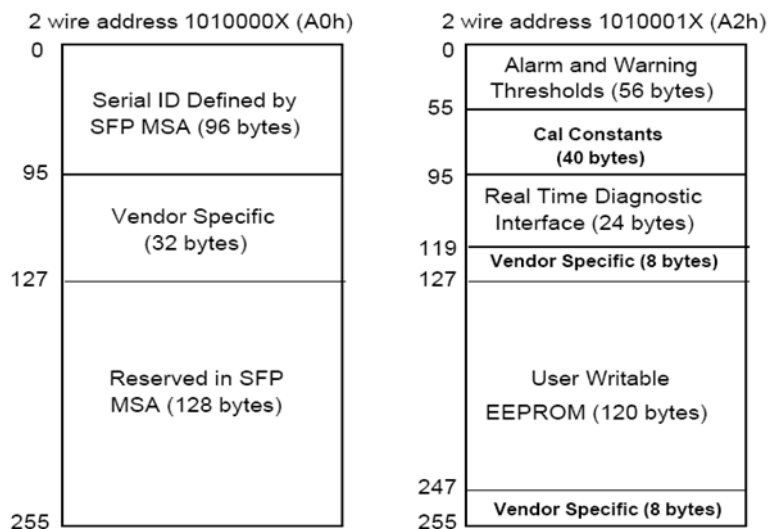
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Recommended Interface Circuit



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Digital Diagnostic Memory Map



7. Modification History

Rev.	Comments	Date	Originator	Approval
01	Preliminary Draft	2025.07.17	Albert Lin	Mike Sun

JPC Connectivity